

LCFC Confidential

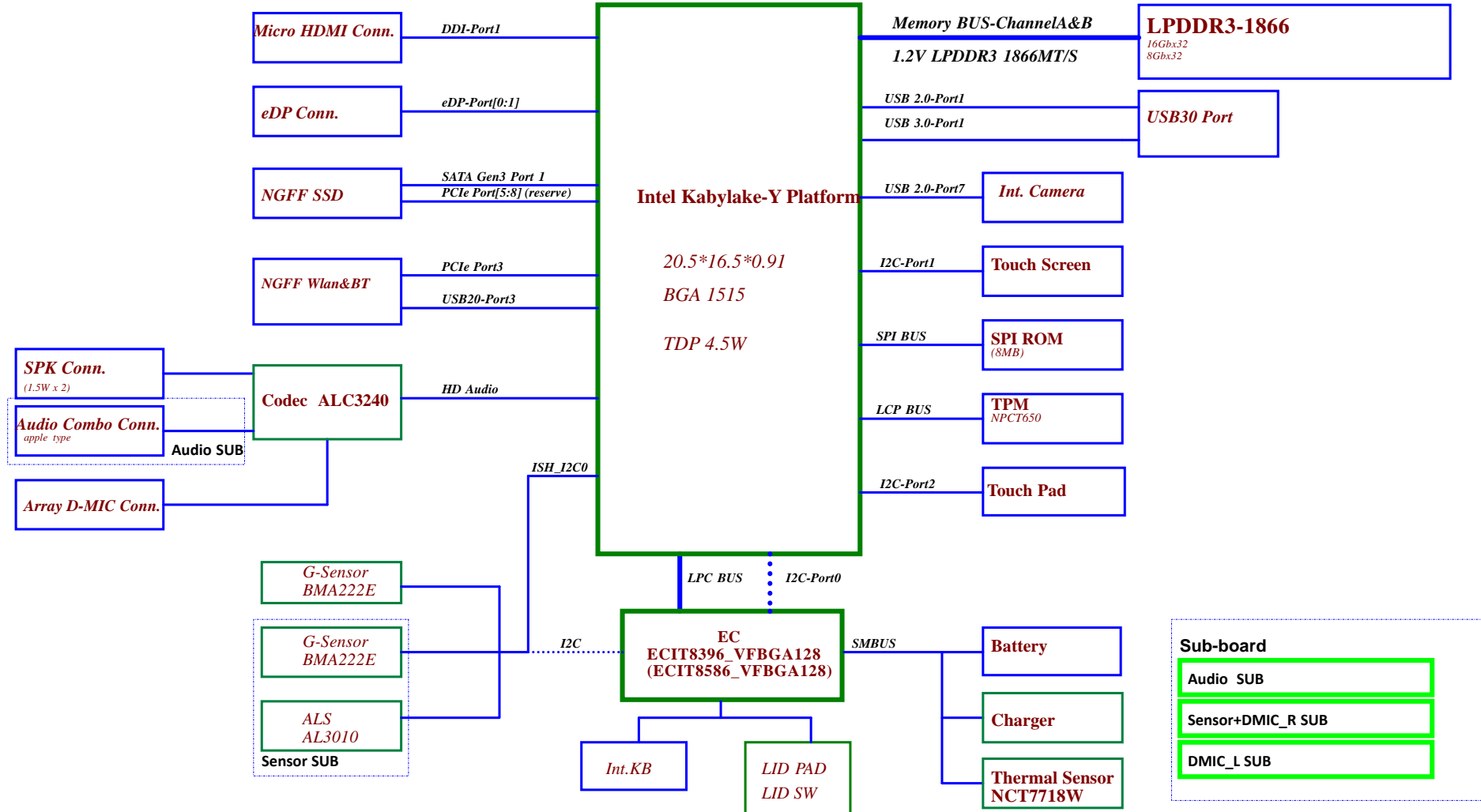
Miray-KBL M/B Schematics Document

INTEL KABYLAKE Mobile ULT Platform
INTEL KBL Y-series CPU + LPDDR3 Memory

2016-06-21

REV:1.0

LCFC-Miary Block diagram



Power Plane State	B+	+3VALW +5VALW +3VL +1.0VALW +1.8VALW +5VLP <small>+CPU_VCCPRIM</small>	+3VALW_PCH	+DDR_1.2V +DDR_1.8V	+5VS +3VS +1.8VS +DDR_0.6VS +CPU_CORE +CPU_VCCIO +CPU_VCCGT +CPU_VCCSA
S0	O	O	O	O	O
S3	O	O	O	O	X
DS3	O	O	X	O	X
S5 S4/AC Only	O	O	O	X	X
S5 S4 Battery only	O	X	X	X	X
S5 S4 AC & Battery don't exist	X	X	X	X	X

SMBUS Control Table

SM Bus address

PCIE PORT LIST

USB Port Table


BOM Structure Table

BOM Structure		BOM Structure	
TI@	TI MIPI camera mount	MIRROR@	EC Mirror-code enable
TPM@	TPM module	UNMIRROR@	EC Mirror-code disable
DEBUG@	DEBUG CARD Part	DA@	PCB
ME@	ME part(connector, hole)	UPI@	UPI MIPI camera mount
RF@	RF request		
EMC@	EMC request		
CD@	COST DOWN Part		
REV@	RESERVER Part		

BOM Configuration Table

X76&VGA Configuration Table

PCB And LOGO Config

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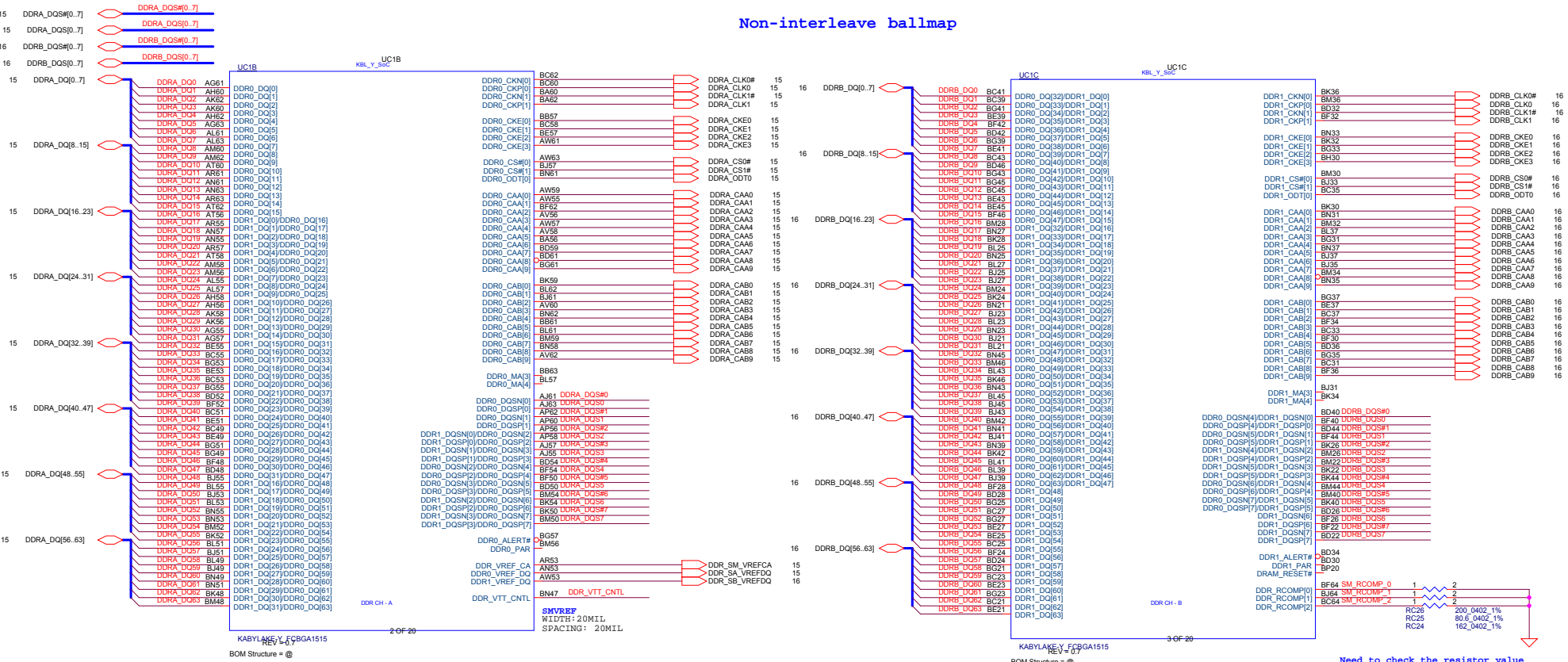
Board ID Table

Board ID	Description	PCB Revision

[illegible][illegible]

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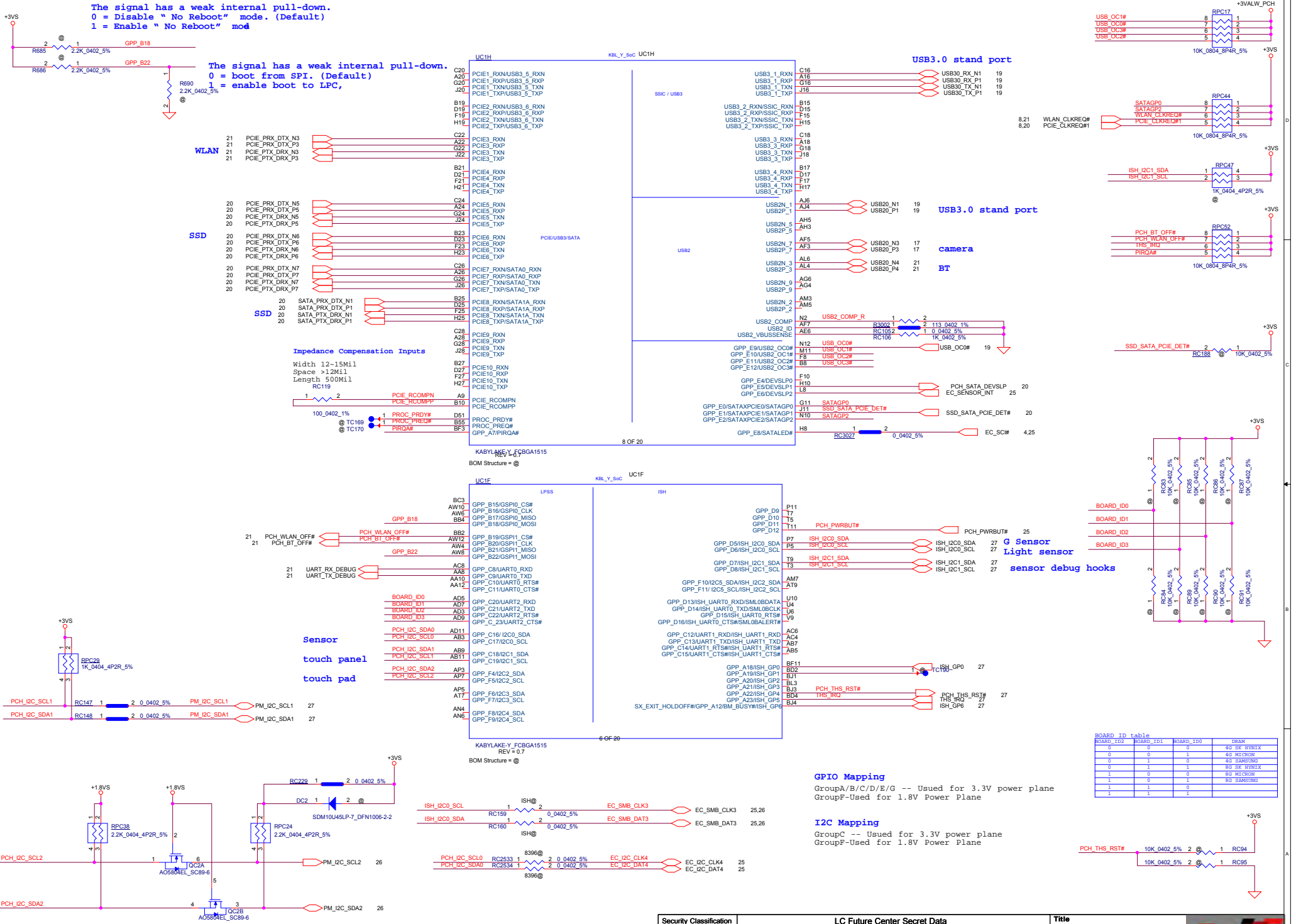
Non-interleave ballmap

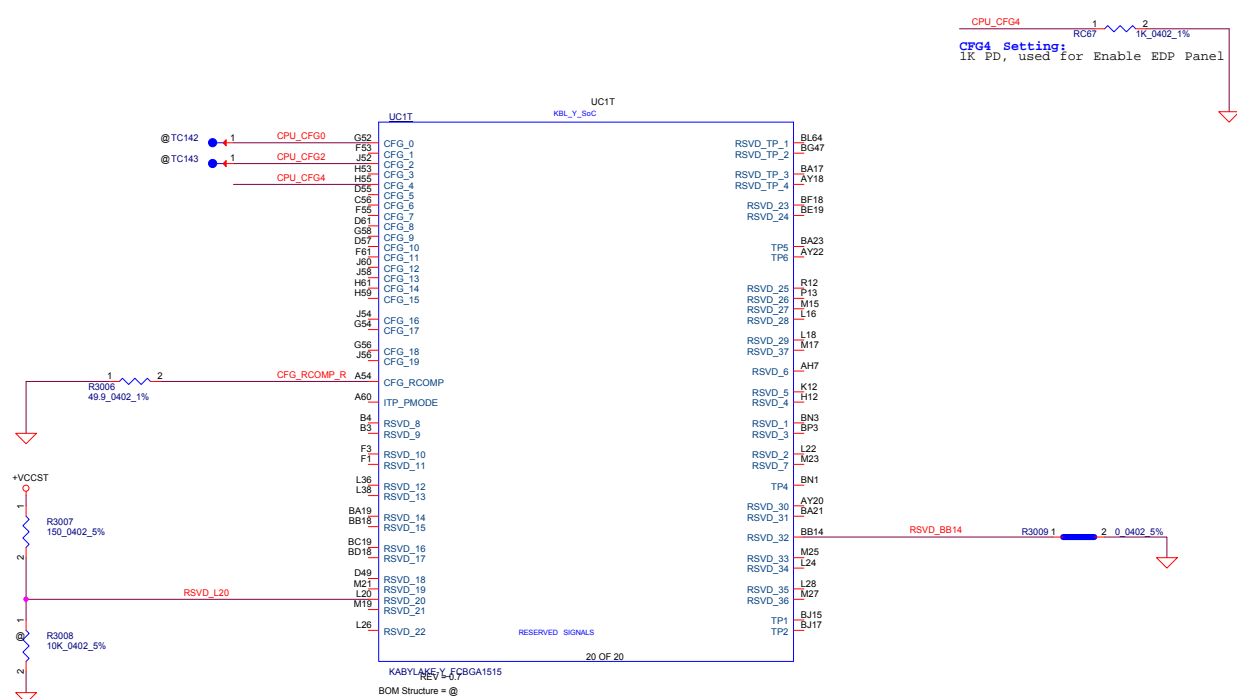




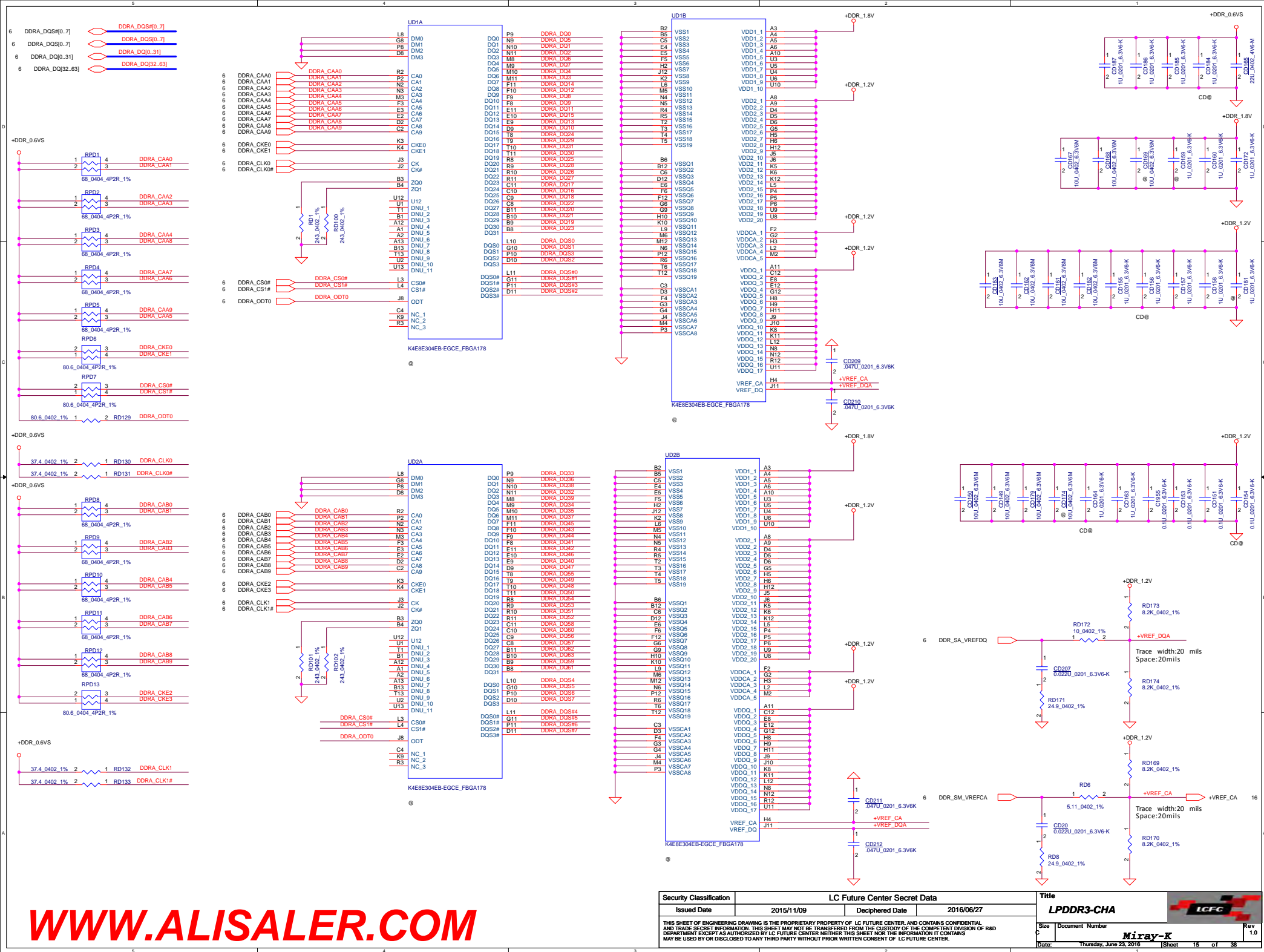
The signal has a weak internal pull-down.
0 = Disable " No Reboot" mode. (Default)
1 = Enable " No Reboot" mod

The signal has a weak internal pull-down.
0 = boot from SPI. (Default)
1 = enable boot to LPC,





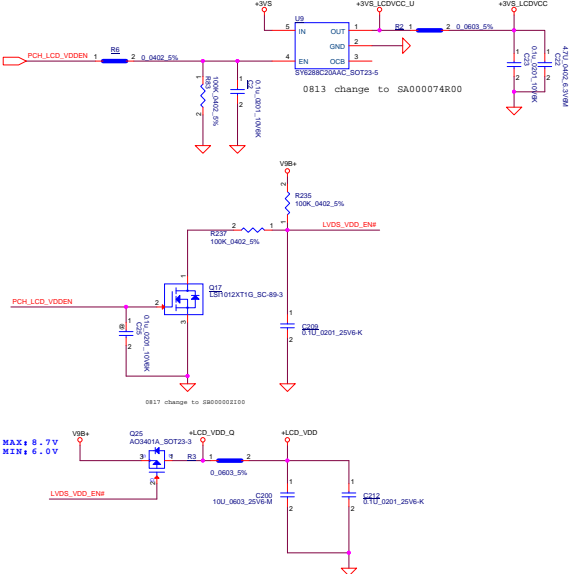






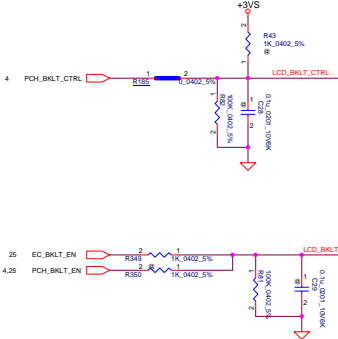
Rev

EDP Panel



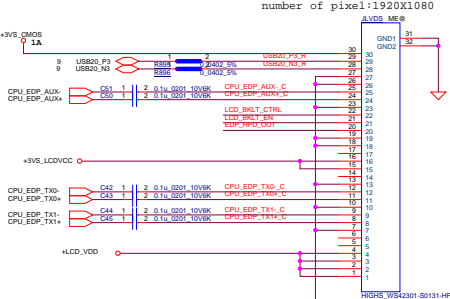
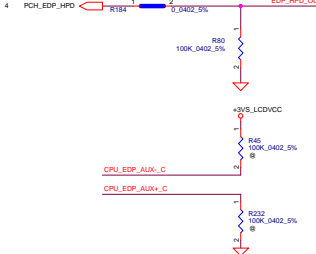
NOTE

1. AC 19V/DC 8.2V
2. AO5800E for VDS concern
3. R235, R237 for VGS concern
4. C200 for voltage concern

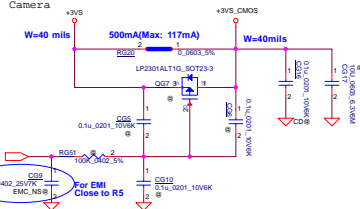


FPC:


1. pin to pin with EDP Panel, check the panel pin definition
2. check the HSYNC for Touch screen board

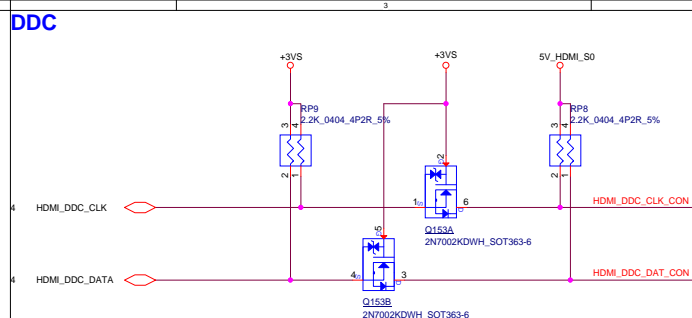
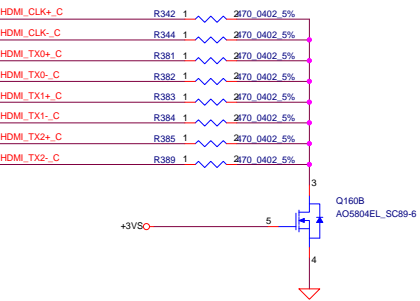
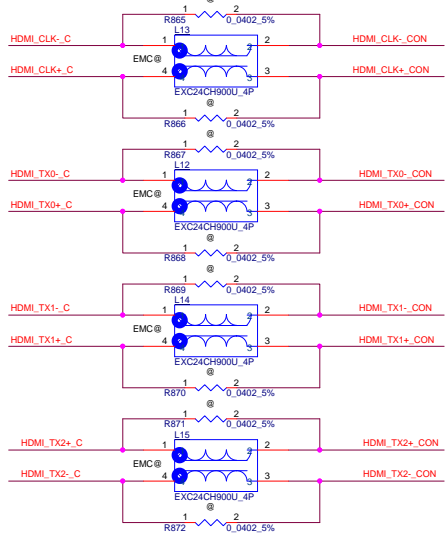
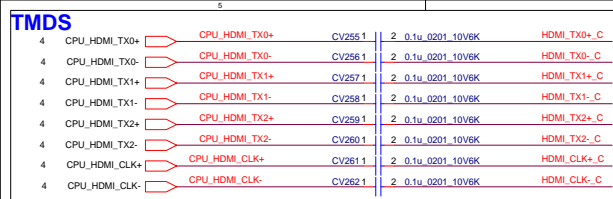


1106 symbol update

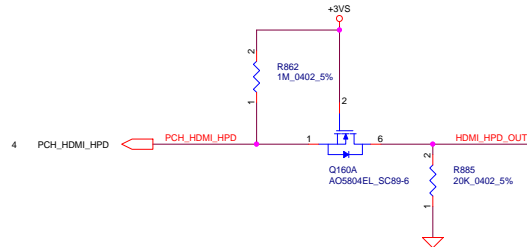


Reserve for other function

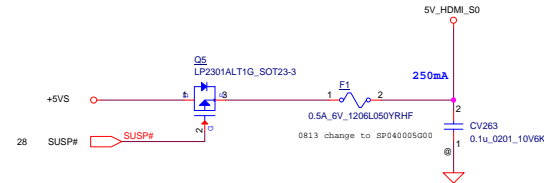
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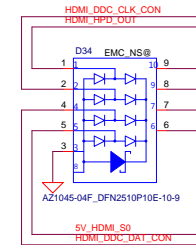
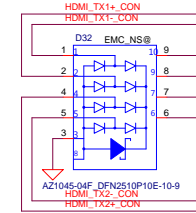
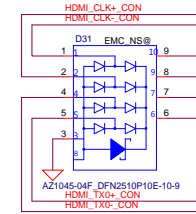
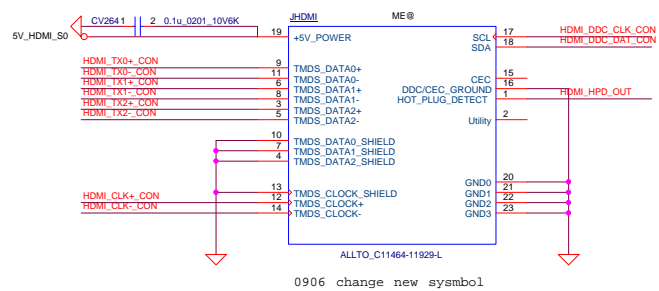
H-PLUG

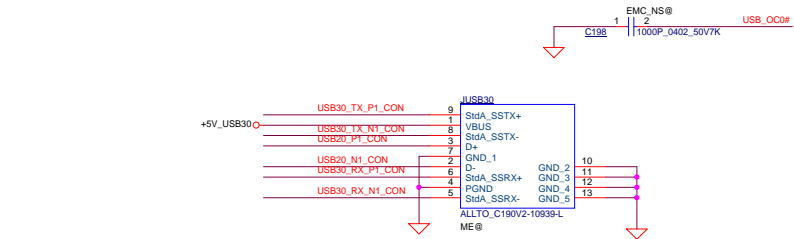
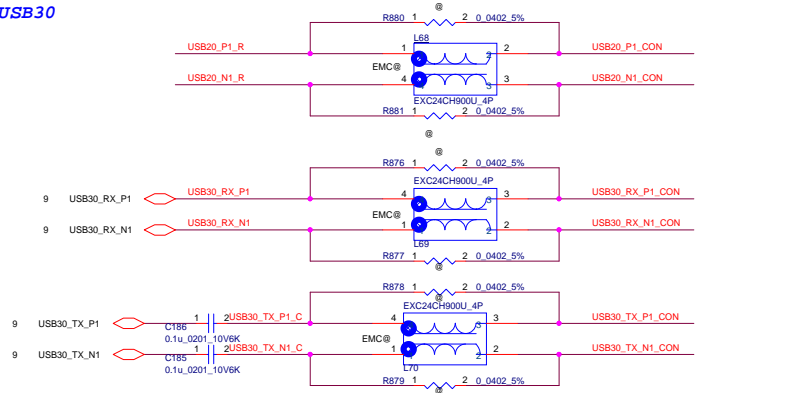


CONN



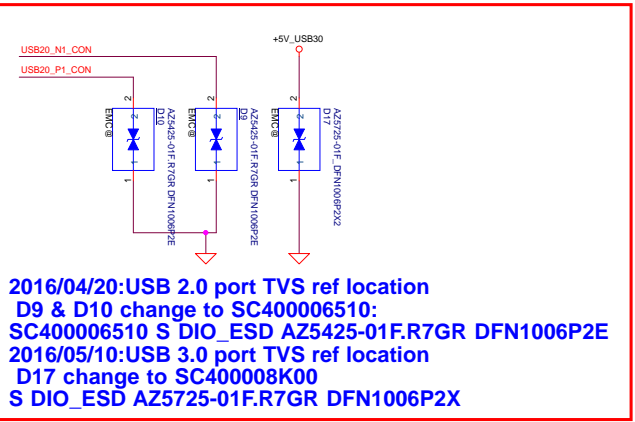
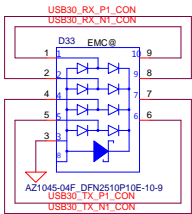
HDMI Type D





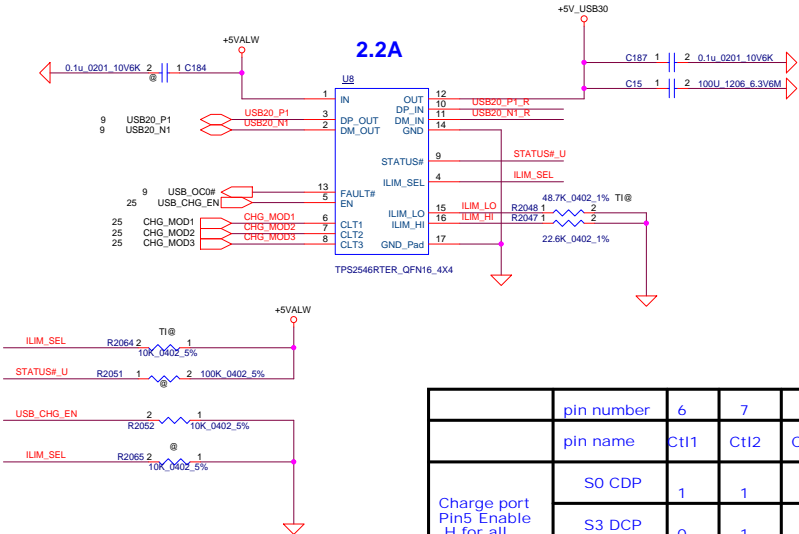
1105 change new symbol

USB type A

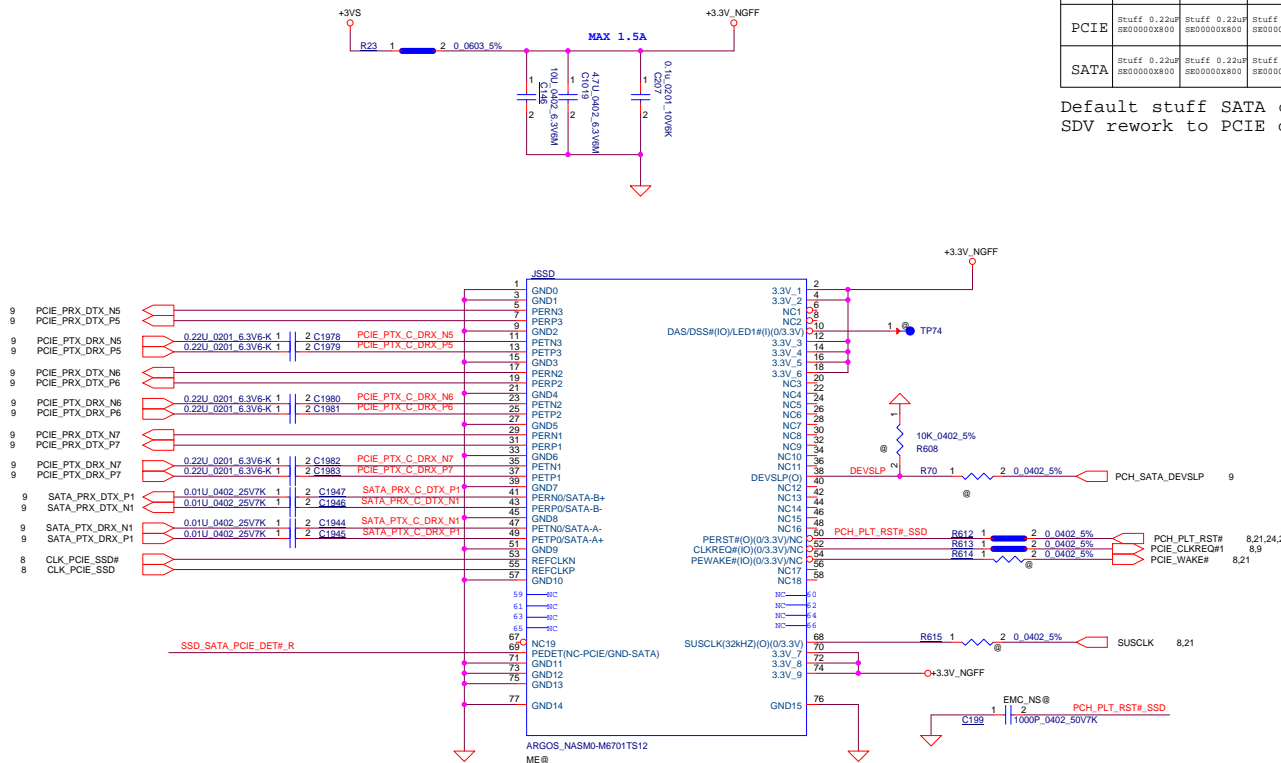


2016/04/20:USB 2.0 port TVS ref location
D9 & D10 change to SC400006510:
SC400006510 S DIO_ESD AZ5425-01F.R7GR DFN1006P2E
2016/05/10:USB 3.0 port TVS ref location
D17 change to SC400008K00
S DIO_ESD AZ5725-01F.R7GR DFN1006P2X

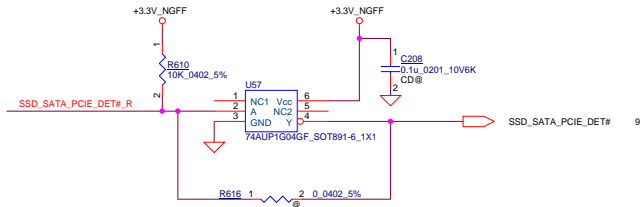
USB charger



	pin number	6	7	8	4
	pin name	Ct11	Ct12	Ct13	ILM_SEL
Charge port Pin5 Enable H for all	S0 CDP	1	1	1	1
	S3 DCP	0	1	1	0/1
	S4/S5 DCP	0	0	1	0/1
Normal port Pin5 Enable H for S0/S3 L for S4/S5	S0 SDP1	1	1	0	0/1
	S3 SDP1	0	1	0	0/1
	S4/S5 Disable	0	0	0	0/1



0827 symbol update

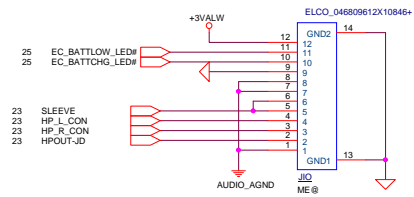


SSD	C1978	C1979	C1980	C1981	C1982	C1983	C1947	C1946	C1944	C1945
PCIE	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.00m SD02800008J	Stuff 0.00m SD02800008J	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800
SATA	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.22uF SE00000X800	Stuff 0.01uF SE075103K8J	Stuff 0.01uF SE075103K8J	Stuff 0.01uF SE075103K8J	Stuff 0.01uF SE075103K8J

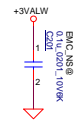
Default stuff SATA option
SDV rework to PCIE option and test SATA/PCIE signal and SSD

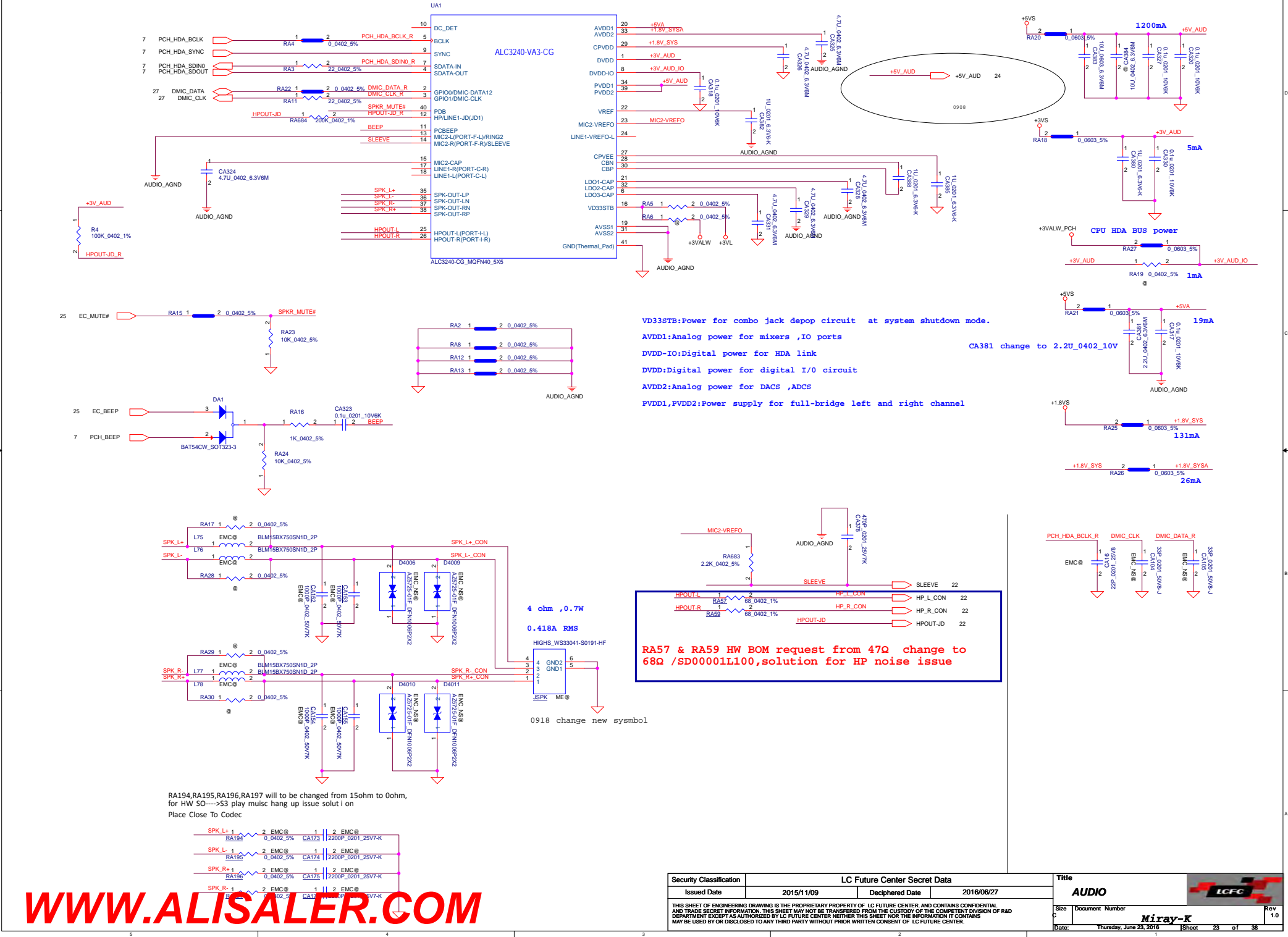
[illegible]

WLAN&BT Combo module circuits		
	BT on module Enable	BT on module Disable
* BT_CTRL	H	L
PCH_BT_ON#	L	H

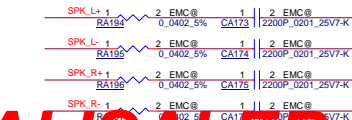


0111 symbol update

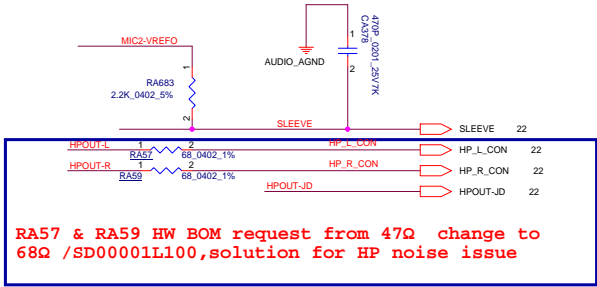




RA194,RA195,RA196,RA197 will to be changed from 150ohm to 0ohm, for HW SO---->S3 play music hang up issue solut i on Place Close To Codec



VD33STB:Power for combo jack depop circuit at system shutdown mode.
AVDD1:Analog power for mixers ,IO ports
DVDD-I0:Digital power for HDA link
DVDD:Digital power for digital I/O circuit
AVDD2:Analog power for DACS ,ADCS
PVDD1,PVDD2:Power supply for full-bridge left and right channel

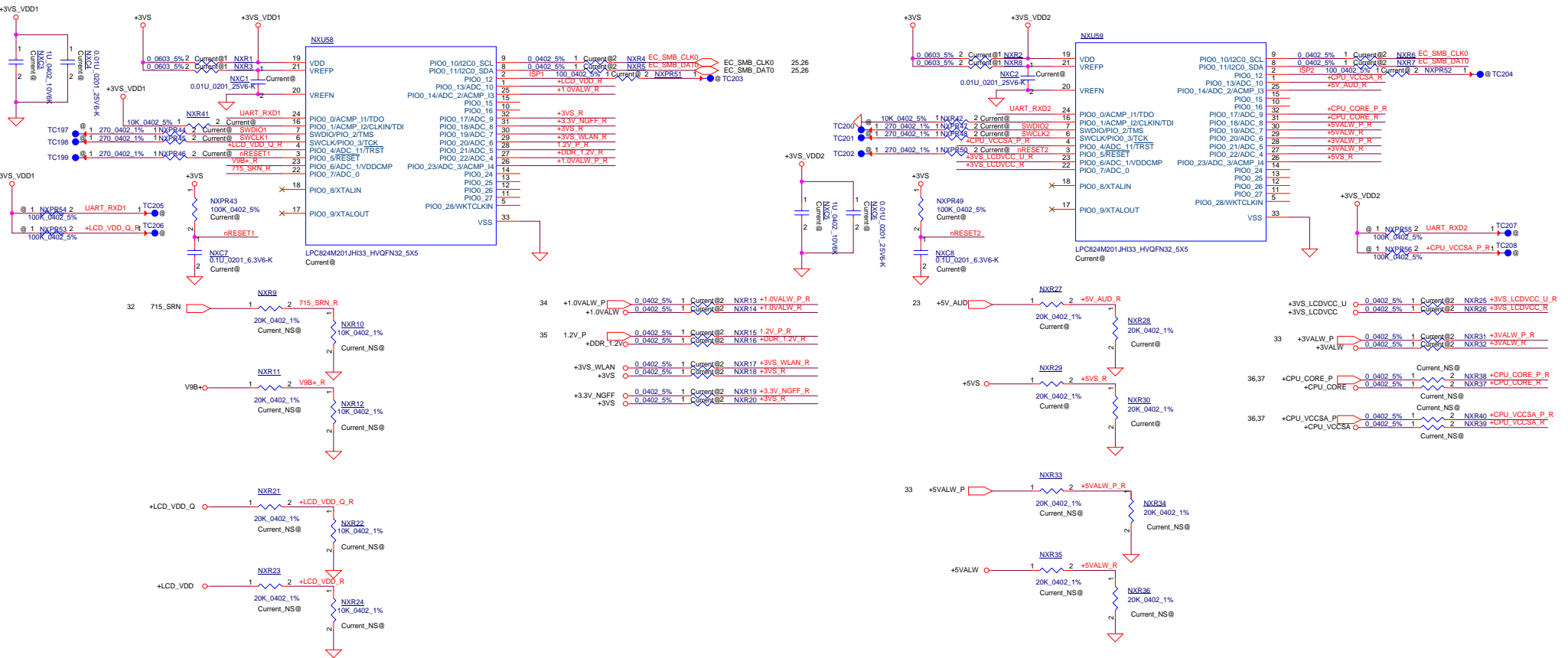
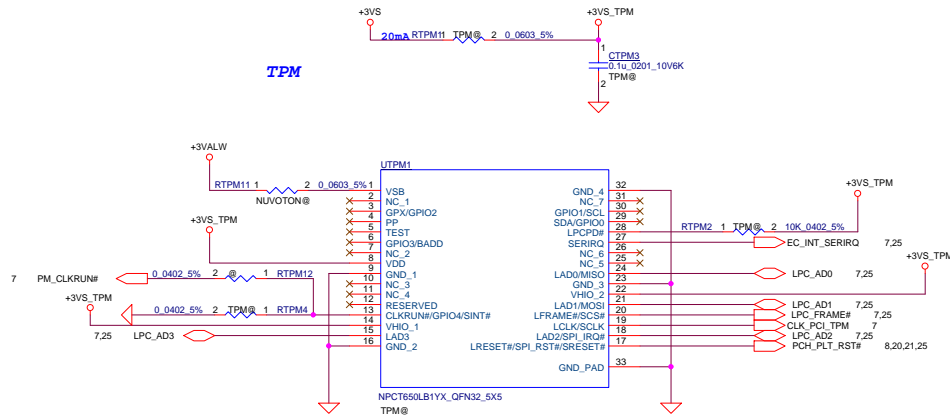


RA57 & RA59 HW BOM request from 47Ω change to 68Ω /SD00001L100,solution for HP noise issue

	Vendor	PN	Describe
国内	NATIONZ	SA000071N00	S IC 232H320TC-LPC-F28-233 TSSOP 28P TPM
WW 1.2	Nuvoton	SA00007R300	S IC NPCT650L1NWX TSSOP 28P TPM1.2
	Nuvoton	SA00007H110	S IC NPCT650L1YX QFN 32P DISCRETE TPM
WW 2.0	Nuvoton	SA00007FW00	S IC NPCT650LAANX TSSOP 28P TPM 2.0

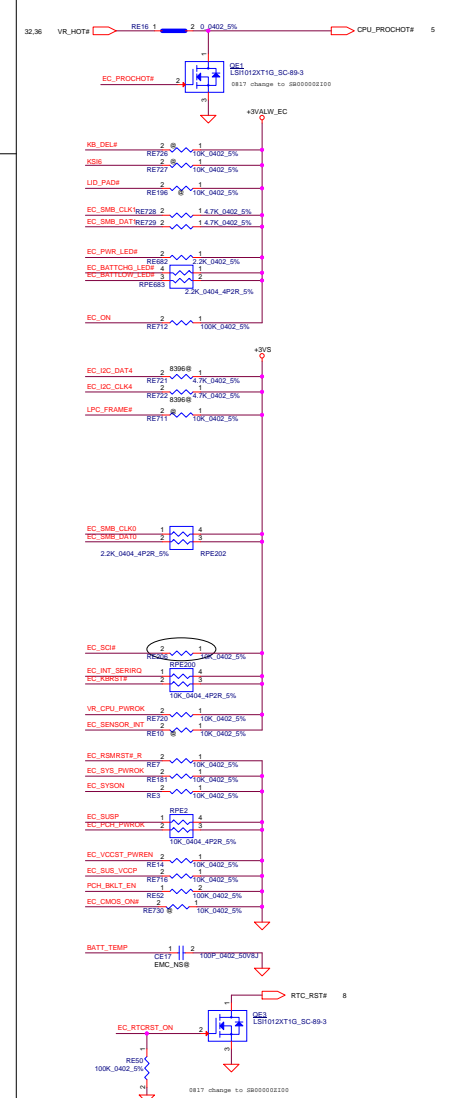
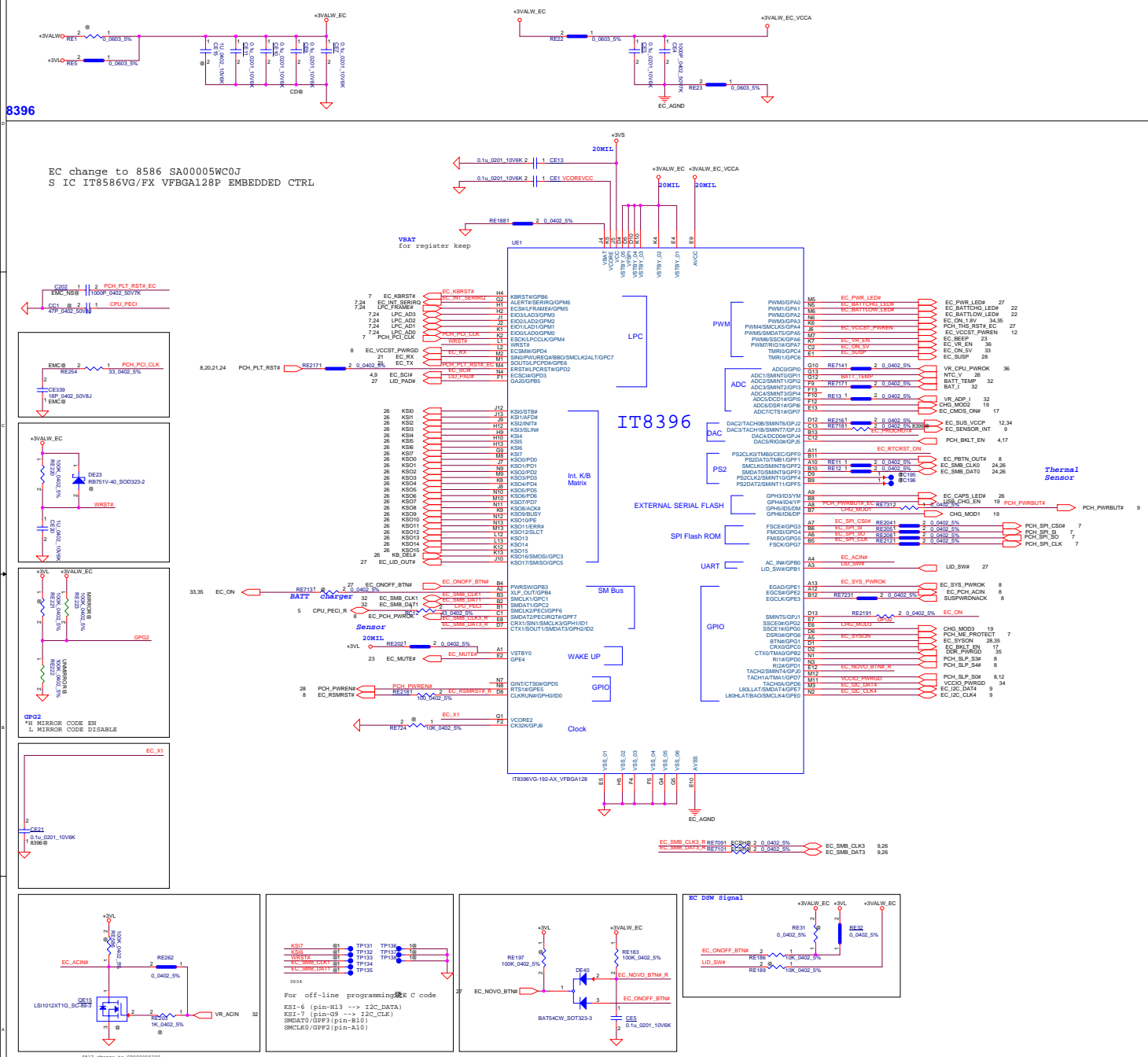
SDV Use WW2.0 Nuvoton
SIV need change to WW1.2 QFN

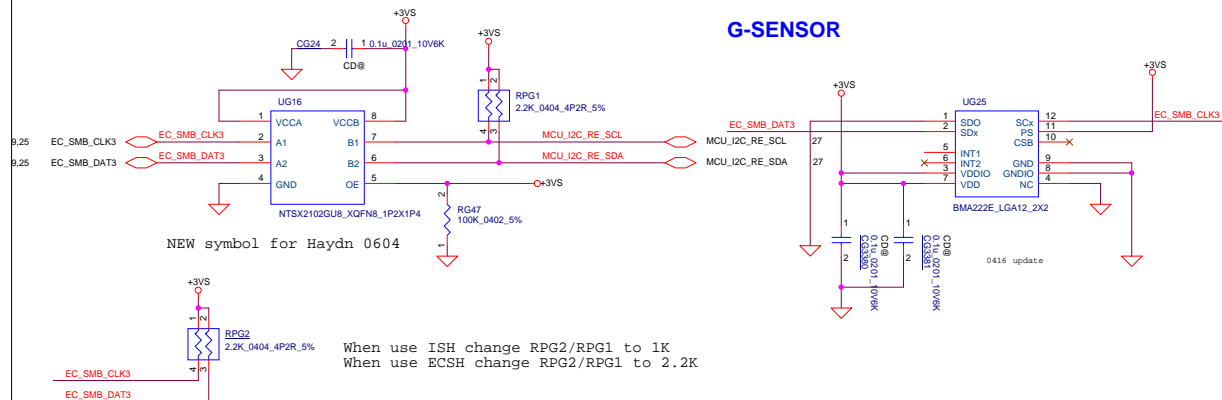
TPM



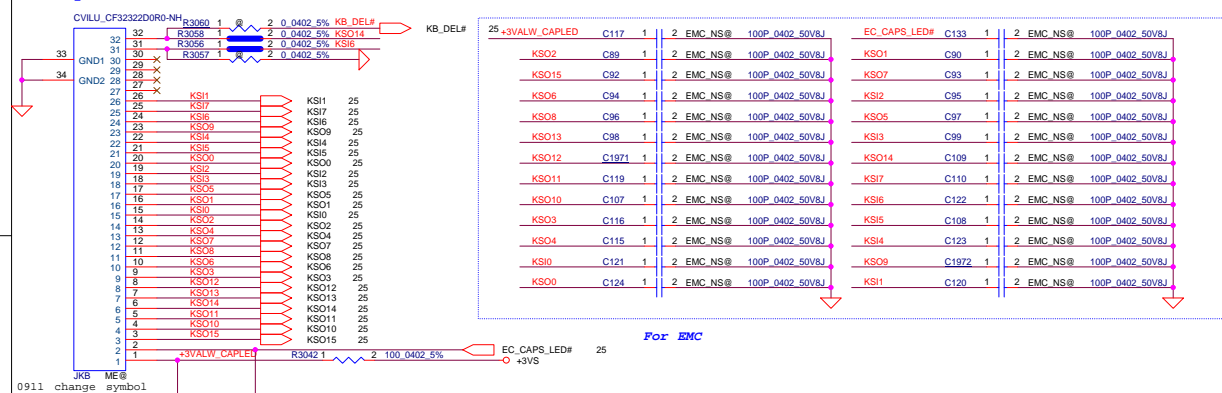
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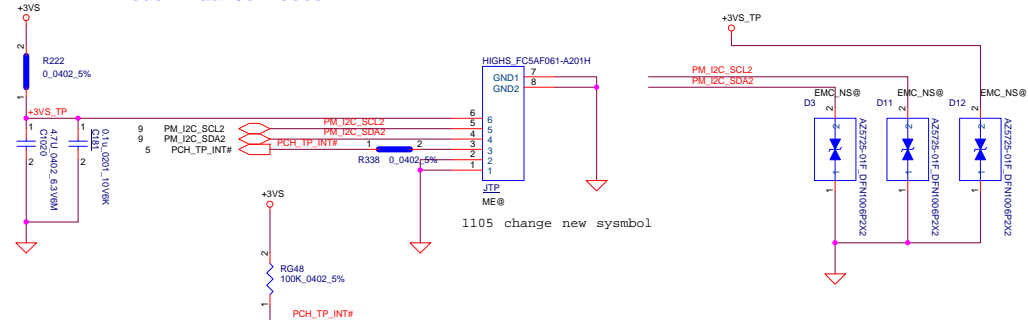




Keyboard Connector

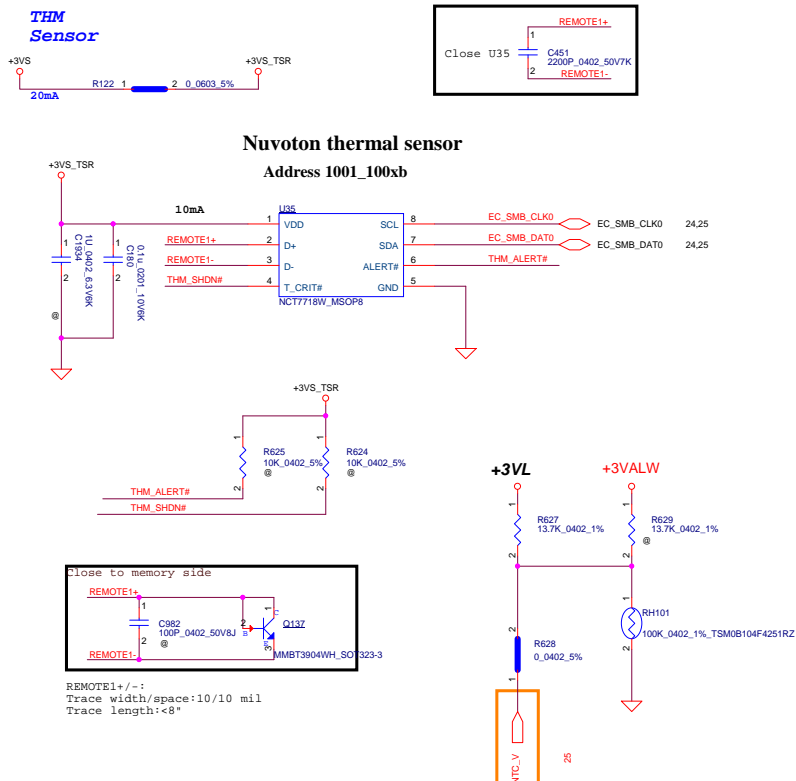


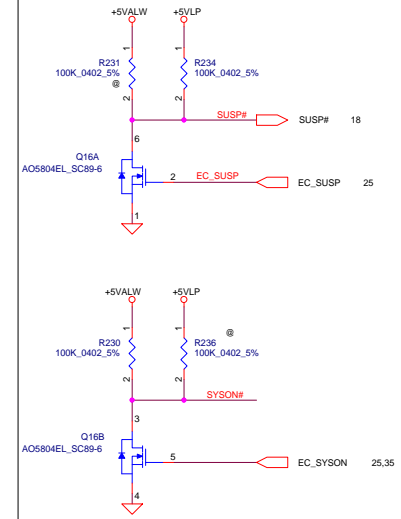
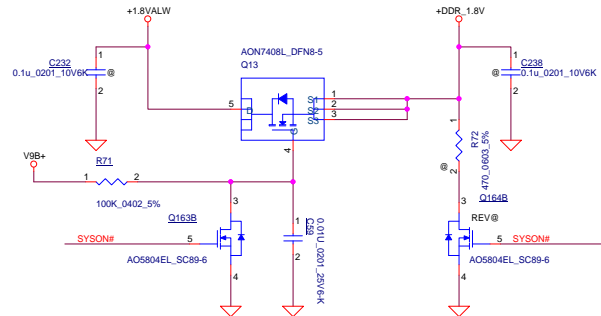
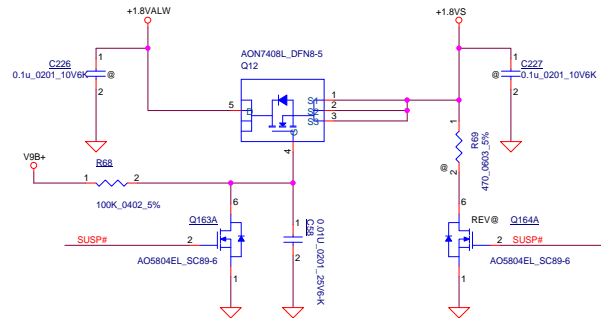
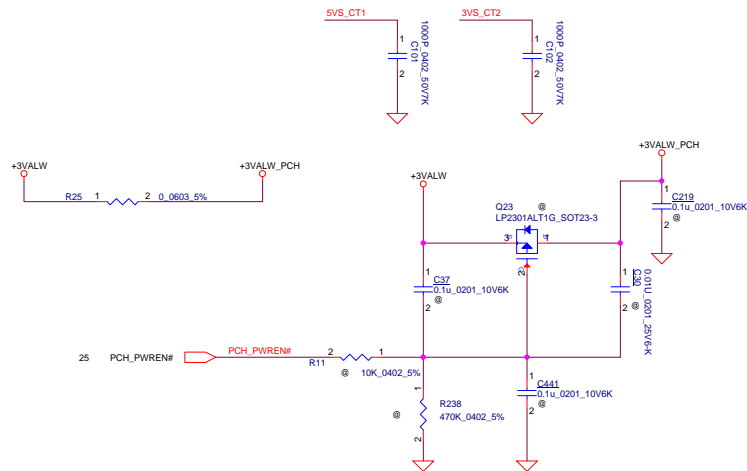
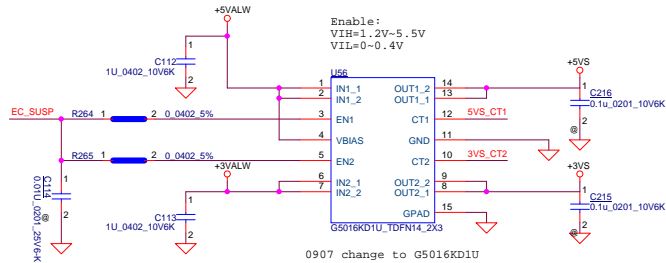
Touch Pad Connector



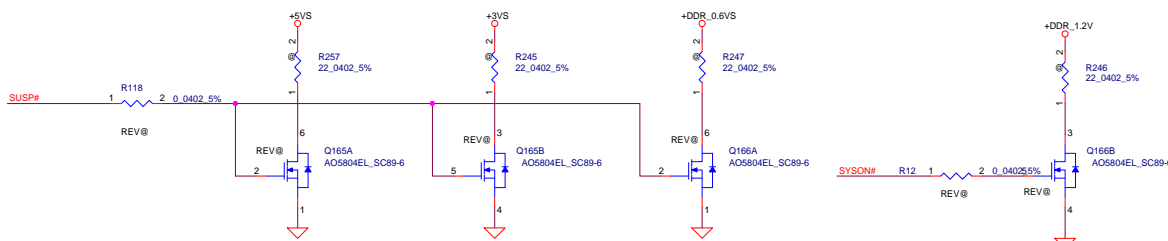
Nuvoton thermal sensor

Address 1001_100xb

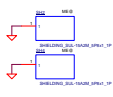
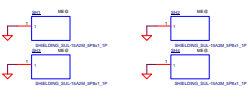
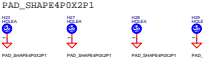
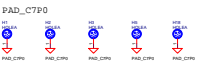




For HDMI signal cross moat concern.




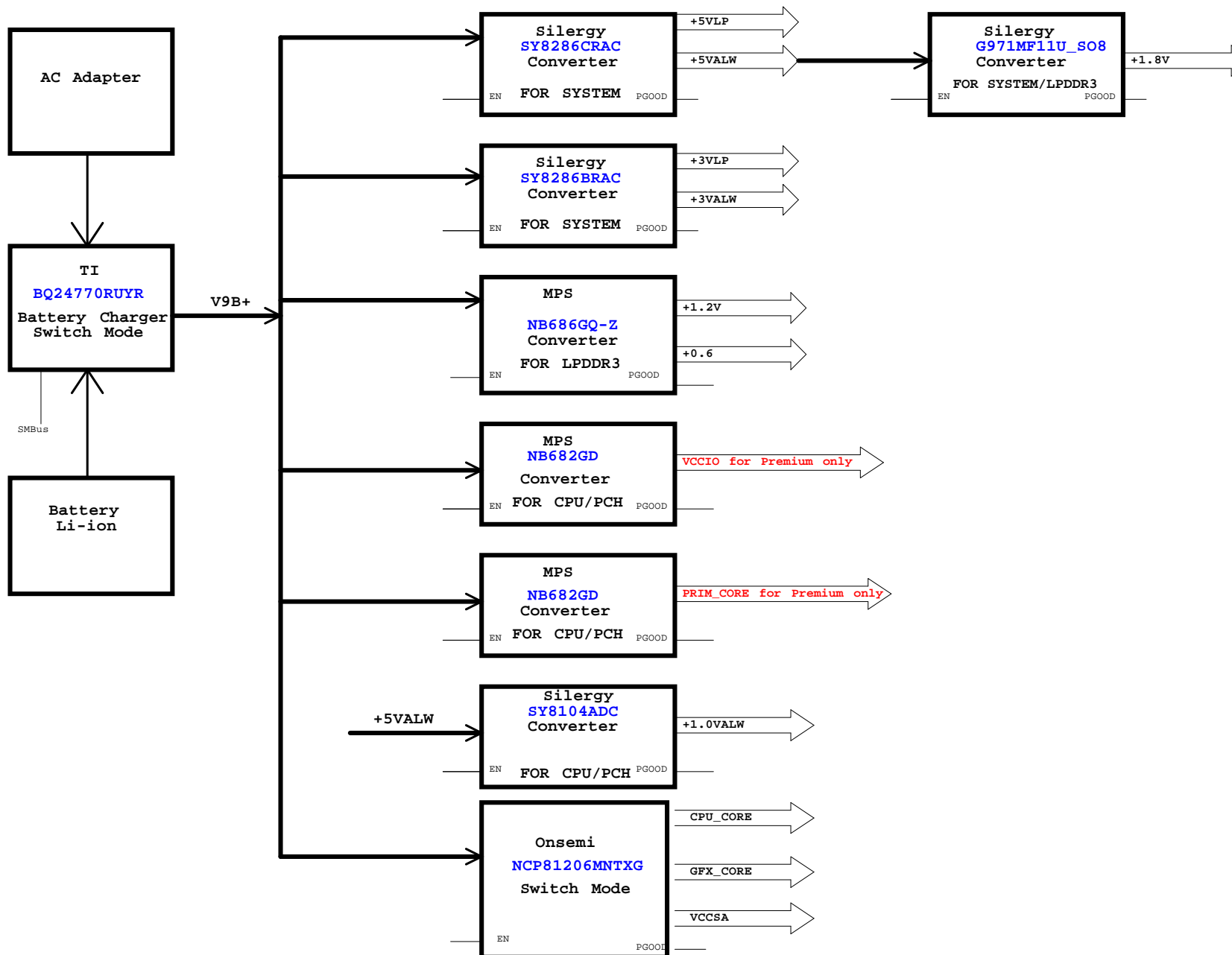
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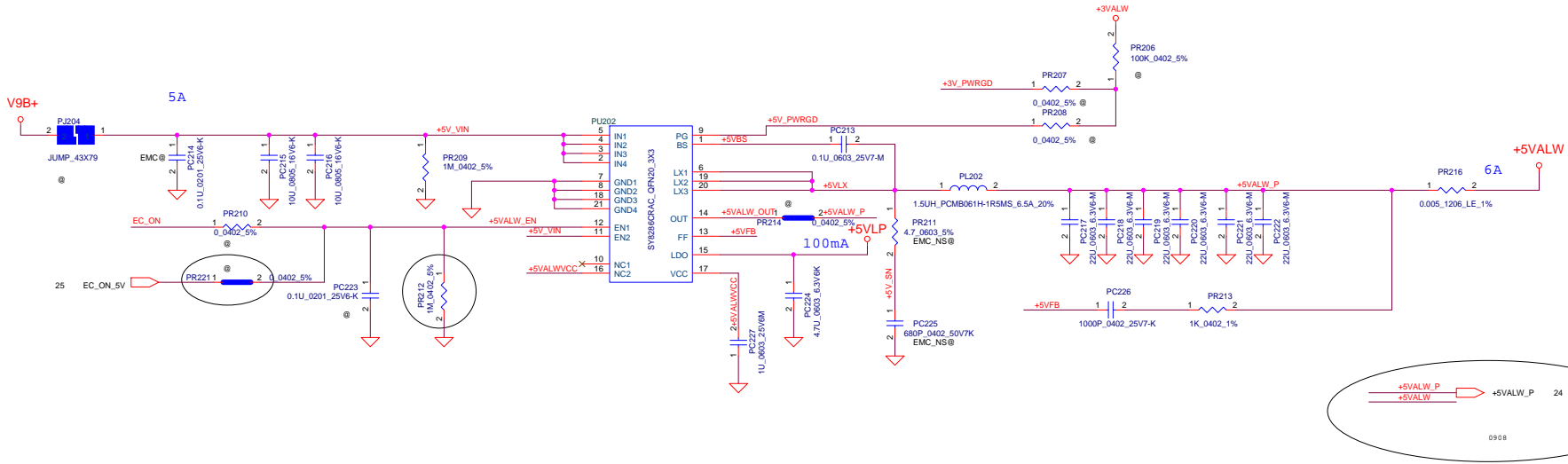
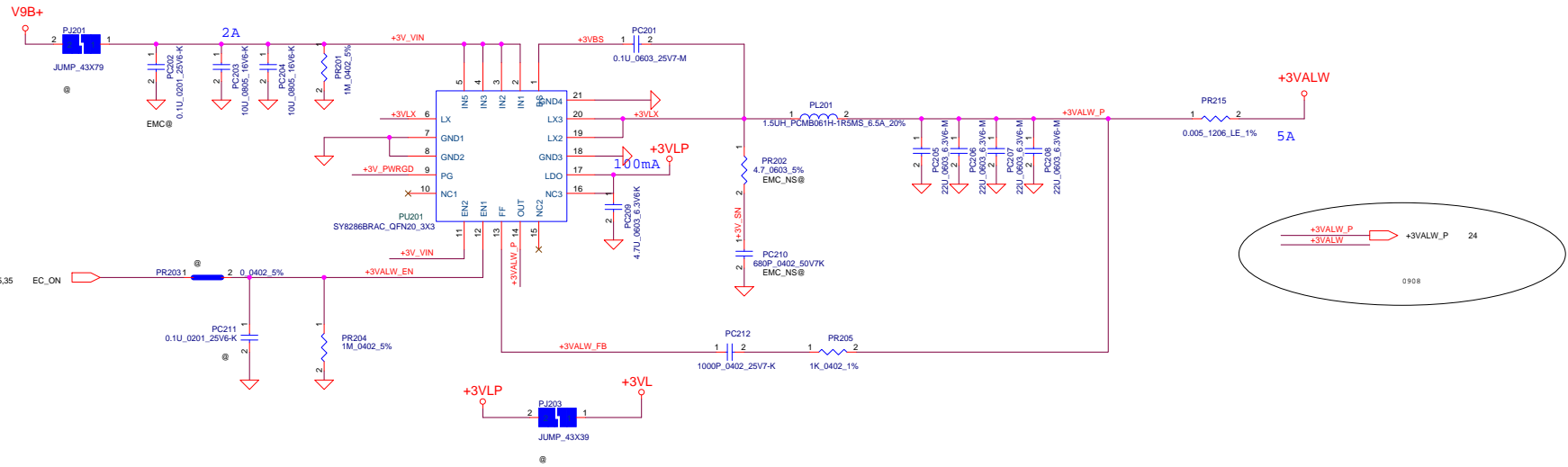


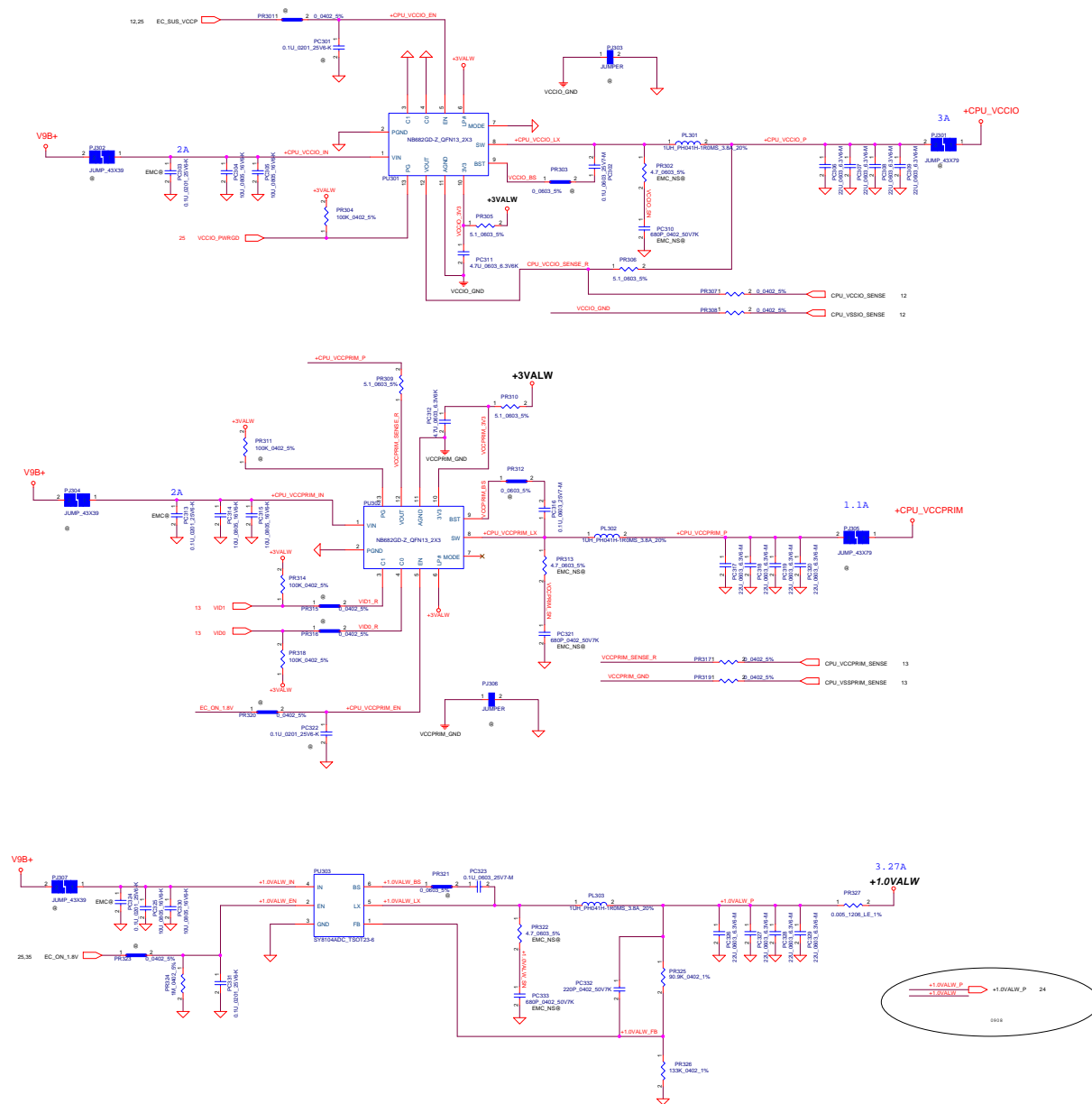
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Follow DXF update Hole

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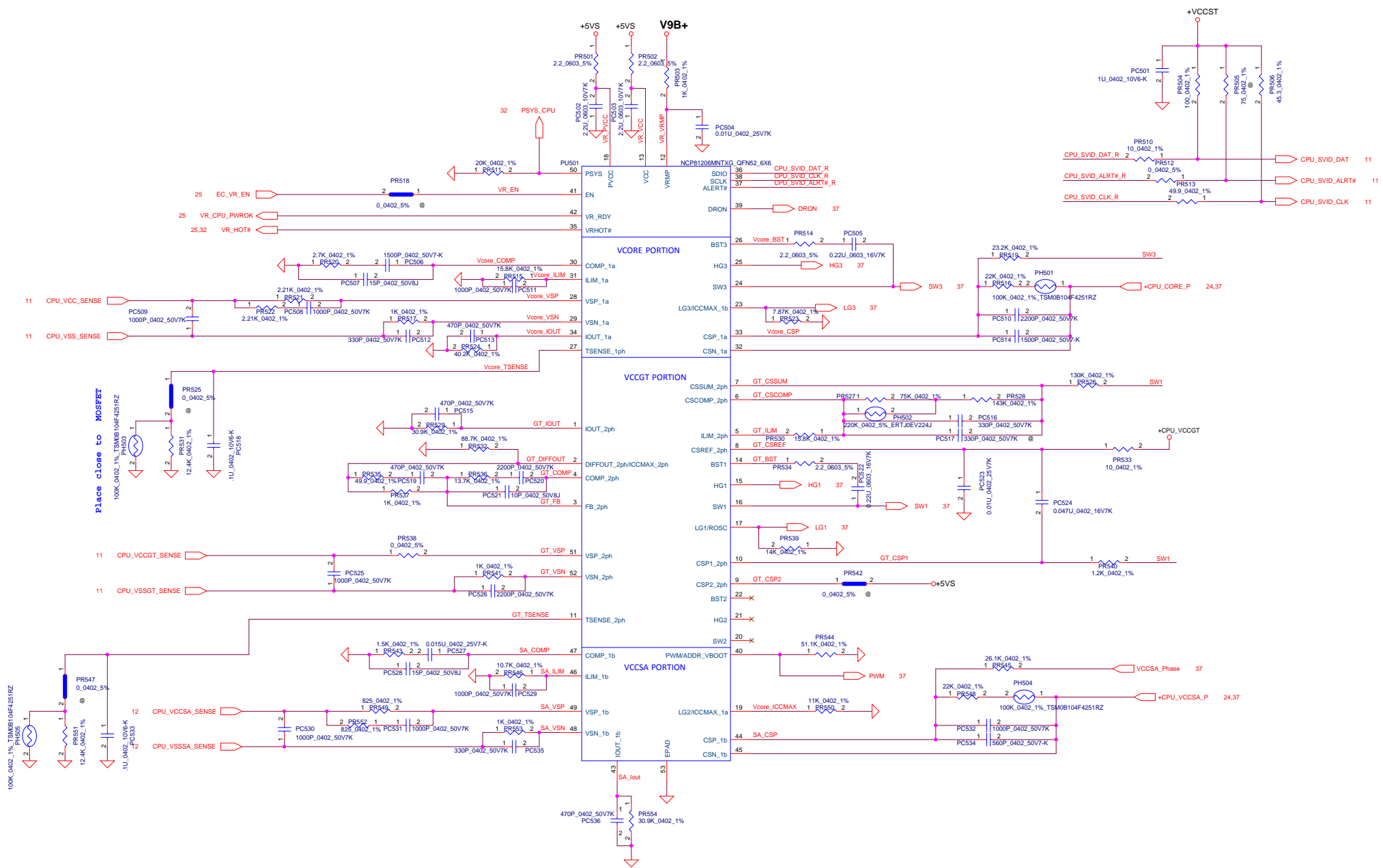
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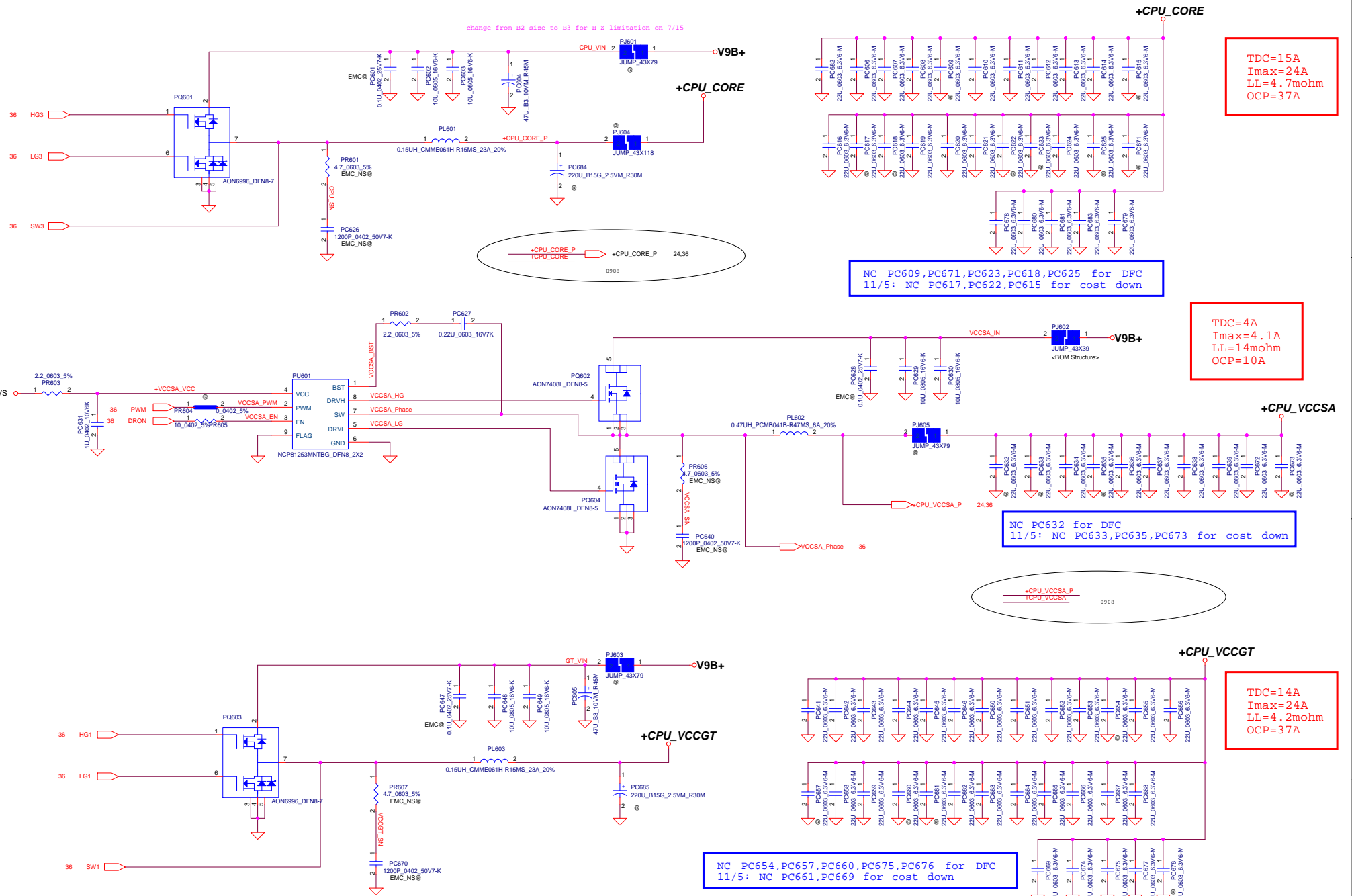





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change from B2 size to B3 for H-Z limitation on 7/15



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